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PTO IDENTIFIER:

Application Number

10/620,456-Conf. #9179

Patent Number

Inventor;

Chin LEE

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FROM:

BIRCH, STEWART, KOLASCH & BIRCH, LLP

Joe McKinney Muncy

PHONE:

(703) 205-8026

Attorney Dkt. #:

4444-0120P

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BIRCH, STEWART, KOLASCH & BIRCH, LLP

8110 Gatehouse Road, Suite 100 East, P.O. Box 747, Falls Church, Virginia 22040-0747

Telephone: (703) 205-8000 Facsimile: (703) 205-8050

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Application No. (if known): 10/620,456

Application No. (if known): 4444-0120P

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MAR 0 8 2007

AMENDMENT TRANSMITTAL LETTER						Docket No. 4444-0120P	
Application No. 10/620,456-Conf. #9179		Filing Date		Examiner		Art Unit	
Applicant(s): Chin LEE		July 17, 2003		P. F. Contino		2114	
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Invention: MEMC	PRY MODELIN	G CIRCUIT W	ITH FAULT	COLERATION .			
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CLAIMS AS AMENDED							
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Jae McKinney M Attorney Reg. No				Dated.	March 6,	2007	
BIRCH, STEWAR	1	2 BIDAU 117	,				
8110 Gatehouse Suite 100 East	Road	d DINON, ELF	-				
P.O. Box 747	/						
Falls Church, Virg (703) 205-8026	ginia 22040-07	747					
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Birch, Stewart, Kolasch & Birch, LLP

MAR 0 8 2007

Docket No.: 4444-0120P

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Chin LEE

Application No.: 10/620,456

Confirmation No.: 9179

Filed: July 17, 2003

Art Unit: 2114

For: MEMORY MODELING CIRCUIT WITH

FAULT TOLERATION

Examiner: P. F. Contino

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

INTRODUCTORY COMMENTS

In reply to the Office Action dated December 8, 2006, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes:

Amendments to the Specification;

Amendments to the Claims;

Amendments to the Drawings; and

Remarks.